



# SREE RAMA ENGINEERING COLLEGE (AUTONOMOUS)

Approved by AICTE, New Delhi – Affiliated to JNTUA, Ananthapuramu  
Accredited by NAAC with 'A' Grade Rami Reddy Nagar, Karakambadi road, Tirupati-517507

F. No: SRET/EC/2024-25/ODD/019

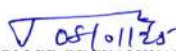
Date: 08-01-2025

**TIME TABLE**  
**M.Tech 1 Year I Semester (SRET24) Regular Examinations January 2025**  
(For 2024 admitted batch only)


Date/Day	STRUCTURAL ENGINEERING	EMBEDDED SYSTEMS	VLSI DESIGN	COMPUTER SCIENCE ENGINEERING
20-01-2025 Monday	(24MTSE01T) THEORY OF ELASTICITY	(24MTES01T) MICROCONTROLLERS AND PROGRAMMABLE DSPS	(24MTVD01T) CMOS ANALOGY IC DESIGN	(24MTCS01T) ADVANCED DATA STRUCTURE AND ALGORITHMS
22-01-2025 Wednesday	(24MTSE02T) ADVANCED STRUCTURAL	(24MTES02T) ADVANCED DIGITAL SYSTEM DESIGN	(24MTVD02T) CMOS DIGITAL IC DESIGN	(24MTCS02T) ADVANCED COMPUTER NETWORK
24-01-2025 Friday	(24MTSE03Tb) ADVANCED CONCRETE	(24MTES03Tb) CMOS DIGITAL IC DESIGN	(24MTVD03Tc) CAD FOR VLSI	(24MTCS03Ta) MACHINE LEARNING
27-01-2025 Monday	(24MTSE04Ta) DESIGN OF PRE-STRESSED CONCRETE	24MTES04Tc SOC ARCHITECTURE	(24MTVD04Tb) FPGA ARCHITECTURES AND APPLICATIONS	(24MTCS04Tb) DESIGN PATTERN
29-01-2025 Wednesday	(24MTBS01T) RESEARCH METHODOLOGY AND IPR	(24MTBS01T) RESEARCH METHODOLOGY AND IPR	(24MTBS01T) RESEARCH METHODOLOGY AND IPR	(24MTBS01T) RESEARCH METHODOLOGY AND IPR

Timings: FN: 10:00 AM to 01:00 PM

Note: If any omissions or clashes in the time table may please be informed to the undersigned immediately.

  
CONTROLLER OF EXAMINATIONS  
SREE RAMA ENGINEERING COLLEGE  
(AUTONOMOUS)

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HOD's: CE, EEE, ME, ECE, CSE, AI&DS, MBA & BS&H, Transport incharge, Website, Notice Boards, PA to Principal

  
CHIEF CONTROLLER OF EXAMINATIONS  
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