

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

M.TECH IN VLSI DESIGN

1.3.4 List of Students Undertaken Field Projects/Internships/student Projects and Supporting Evidences (2021-2022)

SL.NO	ROLL NUMBER	NAME OF THE STUDENT	CLASS & SEMESTER	FIELD PROJECTS/INTERNSHIP/STUDENT PROJECT	END DATES	NAME OF THE ORGANIZATION AND PLACE
1	194C1D5701	BACHU RAJITHA	II M.Tech IV SEMESTER	STUDENT PROJECT	31/12/2021	SREE RAMA ENGINEERING COLLEGE



HEAD OF DEPARTMENT
HEAD
DEPT. OF ELECTRONICS & COMMUNICATION ENGINEERING
SREE RAMA ENGINEERING COLLEGE
KARAKAMBADI ROAD, MANGALAM (B.O.)
TIRUPATI - 517 507

CERTIFICATE

This is to certify that a Project entitled "DESIGN OF A NOVEL LEVEL SHIFTER WITH IMPROVED SPEED AND ENERGY USING A CURRENT LIMITER AND CURRENT MIRROR CONFIGURATION" is the bonafide work done by BACHU RAJITHA (194CID5701), the Department of Electronics and Communication Engineering, SREE RAMA ENGINEERING COLLEGE, Tirupati and is submitted to Jawaharlal Nehru Technological University Anantapur, Anantapuramu in partial fulfillment of the requirements of the award of the degree of MASTER OF TECHNOLOGY IN VLSI DESIGN.

This work has been carried out under the guidance of

Guide:



Mr. P. Giri Babu, M.Tech,
Associate Professor,
Department of ECE,
Sree Rama Engineering College,
Tirupati.

Head of the Department:



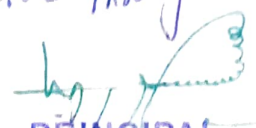
Mr. A.M Guna Sekhar, M.E, MISTE
HOD & Associate Professor,
Department of ECE,
Sree Rama Engineering College,
Tirupati.

Project Viva-Voce Held on 31/12/2021

Internal Examiner

External Examiner

The project viva is conducted through online
on 31/12/2021.


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